

DC-DC Boost Converter Design For Solar Energy Applications

Applications

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PV Hardware System

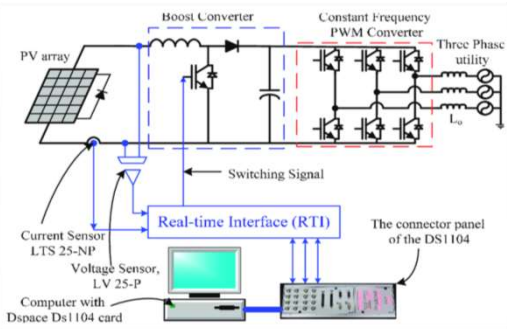


Figure 1. PV Internal Hardware

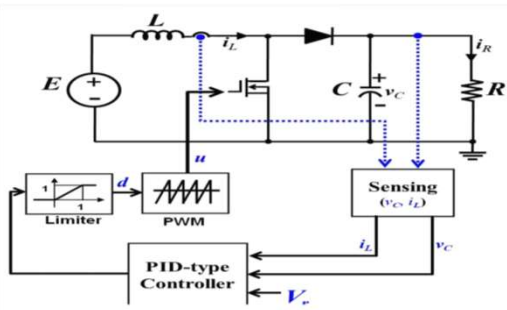


Figure 2. Boost Converter Schematic

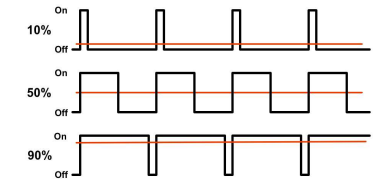


Figure 3. PWM Pulse Visual

References

- Mohan, Ned, et al. *Power Electronics: Converters, Applications, and Design*. John Wiley & Sons, 2003.
- Basic Calculation of a Boost Converter's Power Stage*, www.ti.com/lit/an/slva372c/slva372c.pdf. Accessed 11 Dec. 2023.

Closed Loop Control

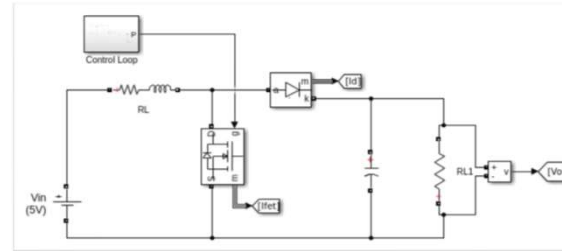


Figure 4. Simulink Design With Closed Loop Control

PID Controller Tuning Method

- Kp, Ki, Kd initially set to zero
- Kp is increased to a critical value where oscillation is stable

Simplified Equations

$$K_p = K_p \quad K_i = K_p / T_i \quad K_d = K_p T_d$$

Proportional gain (Kp), integral gain (Ki), derivative gain (Kd), critical gain (Kcr), critical period (Pcr), integral time (Ti), derivative time (Td)

Type	Kp	Ti	Td
P	0.5Kcr	∞	0
PI	0.45Kcr	Pcr/1.2	0
PID	0.6Kcr	0.5Pcr	0.125Pcr

Figure 4. PID Tuning Method

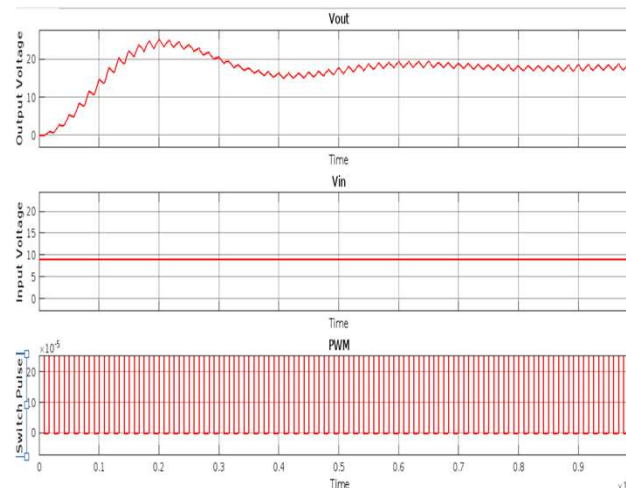
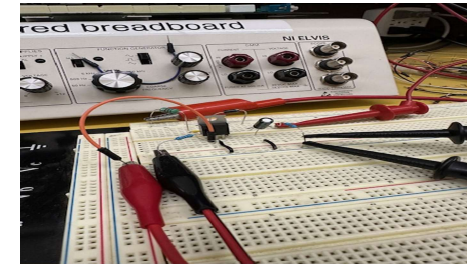


Figure 6. Output Graphs

Design Results



- Error between current output voltage and desired is amplified and sent to PID controller
- PID sums the current, accumulated, and predicted future error and controls PWM switching signal accordingly

- Voltage successfully boosted from 4 volts to 7.94 volts with 22% duty cycle

Why It Matters

- Boost converters allow panels to customize voltage based on need and save excess energy
- Protects all downstream components and improves panel lifetime
- Optimal panel performance and lifetime allow for maximum benefit to the environment

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